

METHOD AND SYSTEM FOR CONTROLLING DEFAULT VALUES OF FLIP-
FLOPS IN PGA/ASIC-BASED DESIGNS

ABSTRACT

During a reset condition or prior to system initialization of an FPGA-based system (100), a FPGA (102) can be pre-configured by loading a value from a memory cell (108) into at least one flip-flop (312) of the FPGA, which represents a configuration register for an FPGA memory controller (106). The FPGA memory controller can be configured using the value loaded in the flip-flop. The value loaded into the flip-flop from the memory cell can be a default value previously stored in the memory cell.